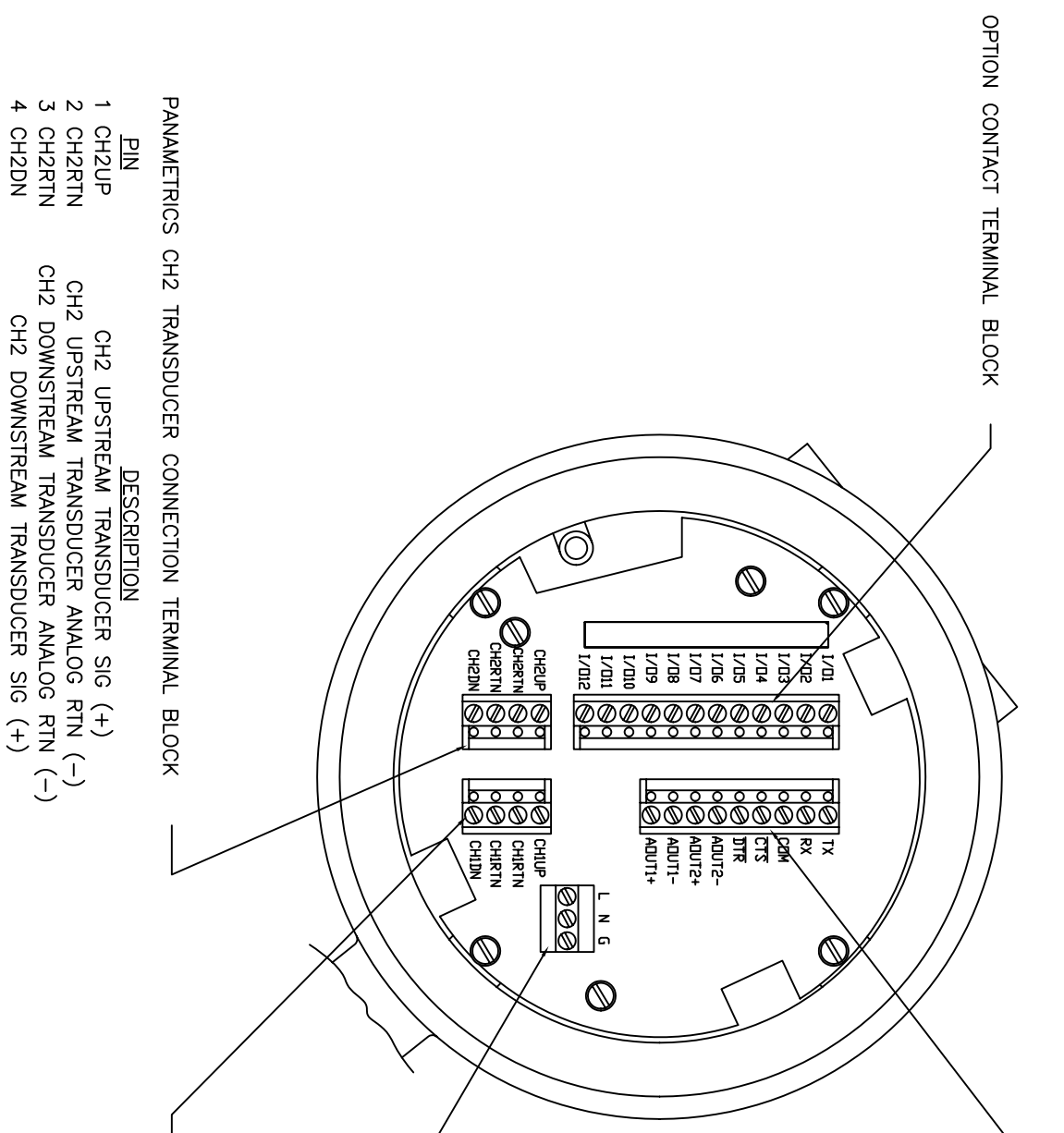


REVISIONS					
REV	ECO	DESCRIPTIONS	DWN	CKD	APVD
1	<i>JK</i>	ORIGINATED FOR REV CONTRL	CJS 10/17/06	C. FRAIL 10/17/06	C. FRAIL 10/17/06

PIN	DESCRIPTION
1 TX	TRANSMIT
2 RX	RECEIVE
3 COM	COMMON
4 CTS	CLEAR TO SEND
5 DTR	DATA TERMINAL READY
6 AOUT2-	4-20 OUTPUT 2 RTN
7 AOUT2+	4-20 OUTPUT 2 SIG
8 AOUT1-	4-20 OUTPUT 1 RTN
9 AOUT1+	4-20 OUTPUT 1 SIG

PIN	DESCRIPTION	SUPPLY VOLTAGE
1 L	LINE POWER CONNECTION	90-260VAC 50/60HZ
2 N	NEUTRAL CONNECTION	
3 G	EARTH GROUND CONNECTION	



PIN	DESCRIPTION
1 CH2UP	CH2 UPSTREAM TRANSDUCER SIG (+)
2 CH2RTN	CH2 UPSTREAM TRANSDUCER ANALOG RTN (-)
3 CH2RTN	CH2 DOWNSTREAM TRANSDUCER ANALOG RTN (-)
4 CH2DN	CH2 DOWNSTREAM TRANSDUCER SIG (+)

PIN	DESCRIPTION
1 CH1UP	CH1 UPSTREAM TRANSDUCER SIG (+)
2 CH1RTN	CH1 UPSTREAM TRANSDUCER ANALOG RTN (-)
3 CH1RTN	CH1 DOWNSTREAM TRANSDUCER ANALOG RTN (-)
4 CH1DN	CH1 DOWNSTREAM TRANSDUCER SIG (+)

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
FRACTIONS ARE IN INCHES
DECIMALS
ANGLES
SURFACE FINISH

THIRD ANGLE PROJECTION

GE Industrial, Sensing
1100 Technology Park Dr.
Billerica, MA 01821 USA

XMT8681, XGM8681, XGS8681
DUAL CHANNEL
AC WIRING DIAGRAM

DRAWING NUMBER 702-686

REV 1

SCALE 1:1 DO NOT SCALE DWG SHEET 1 OF 1

OPTION CONTACT TERMINAL BLOCK

RS232/RS485/4-20 ANALOG OUTPUT TERMINAL BLOCK

AC POWER TERMINAL BLOCK

PANAMETRICS CH2 TRANSDUCER CONNECTION TERMINAL BLOCK

PANAMETRICS CH1 TRANSDUCER CONNECTION TERMINAL BLOCK