

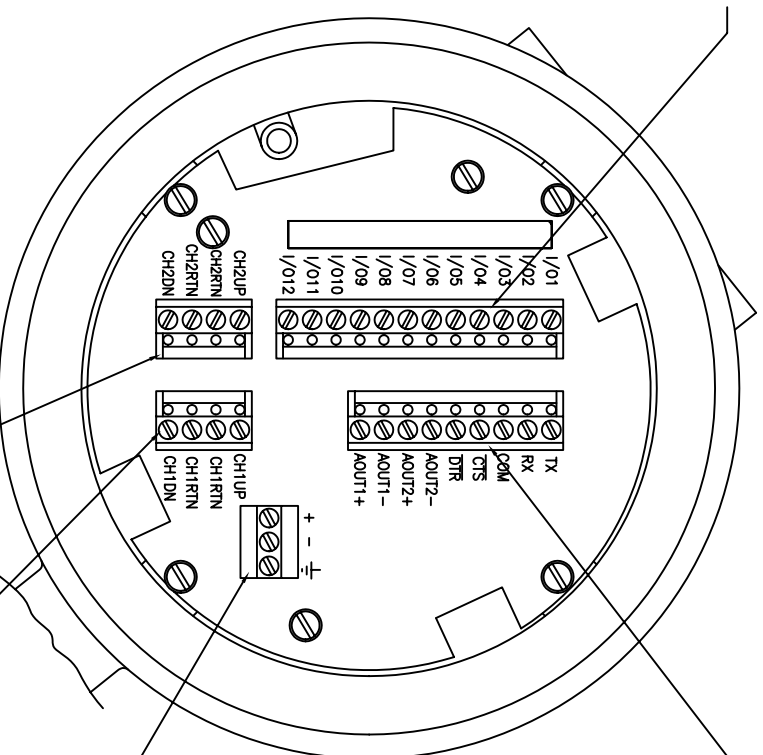
REVISIONS					
REV	ECO	DESCRIPTIONS	DWN	CKD	APVD
1	✓	ORIGINATED FOR REV CONTROL	10/17/06	S. FRALL	S. FRALL

PIN	DESCRIPTION
1 TX	TRANSMIT
2 RX	RECEIVE
3 COM	COMMON
4 CTS	CLEAR TO SEND
5 DTR	DATA TERMINAL READY
6 AOUT2-	4-20 OUTPUT 2 RTN
7 AOUT2+	4-20 OUTPUT 2 SIG
8 AOUT1-	4-20 OUTPUT 1 RTN
9 AOUT1+	4-20 OUTPUT 1 SIG

PIN	DESCRIPTION
1 +	POSITIVE TERMINAL CONNECTION
2 -	NEGATIVE TERMINAL CONNECTION
3 ⚡	GROUND TERMINAL CONNECTION

PIN	DESCRIPTION
1 CH2UP	CH2 UPSTREAM TRANSDUCER SIG (+)
2 CH2RTN	CH2 UPSTREAM TRANSDUCER ANALOG RTN (-)
3 CH2RTN	CH2 DOWNSTREAM TRANSDUCER ANALOG RTN (-)
4 CH2DN	CH2 DOWNSTREAM TRANSDUCER SIG (+)

PIN	DESCRIPTION
1 CH1UP	CH1 UPSTREAM TRANSDUCER SIG (+)
2 CH1RTN	CH1 UPSTREAM TRANSDUCER ANALOG RTN (-)
3 CH1RTN	CH1 DOWNSTREAM TRANSDUCER ANALOG RTN (-)
4 CH1DN	CH1 DOWNSTREAM TRANSDUCER SIG (+)



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES. TOLERANCES ARE DECIMALS ANGLES ± 1/32° .XX ± .01 .XXX ± .005 SURFACE FINISH 125

THIRD ANGLE PROJECTION

DRWN CDS 10/17/06 APPD 10/17/06
 C.FRALL C.FRALL
 CHECKED 10/17/06 SALES

GE Industrial, Sensing
 1100 Technology Park Dr.
 Billerica, MA 01821 USA

XMT8681, XGM8681, XGS8681
 DUAL CHANNEL
 DC WIRING DIAGRAM

SIZE C DRAWING NUMBER 702-688
 SCALE 1:1 DO NOT SCALE DWG SHEET 1 OF 1

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D C B A

OPTION CONTACT TERMINAL BLOCK

RS232/RS485/4-20 ANALOG OUTPUT TERMINAL BLOCK

PANAMETRICS CH2 TRANSDUCER CONNECTION TERMINAL BLOCK

PANAMETRICS CH1 TRANSDUCER CONNECTION TERMINAL BLOCK

12-28VDC POWER TERMINAL BLOCK